### CSE350

### [DIGITAL ELECTRONICS AND PULSE TECHNIQUES](https://bux.bracu.ac.bd/courses/course-v1:buX+CSE350+2021_Summer/course/)

**Lab- 03**

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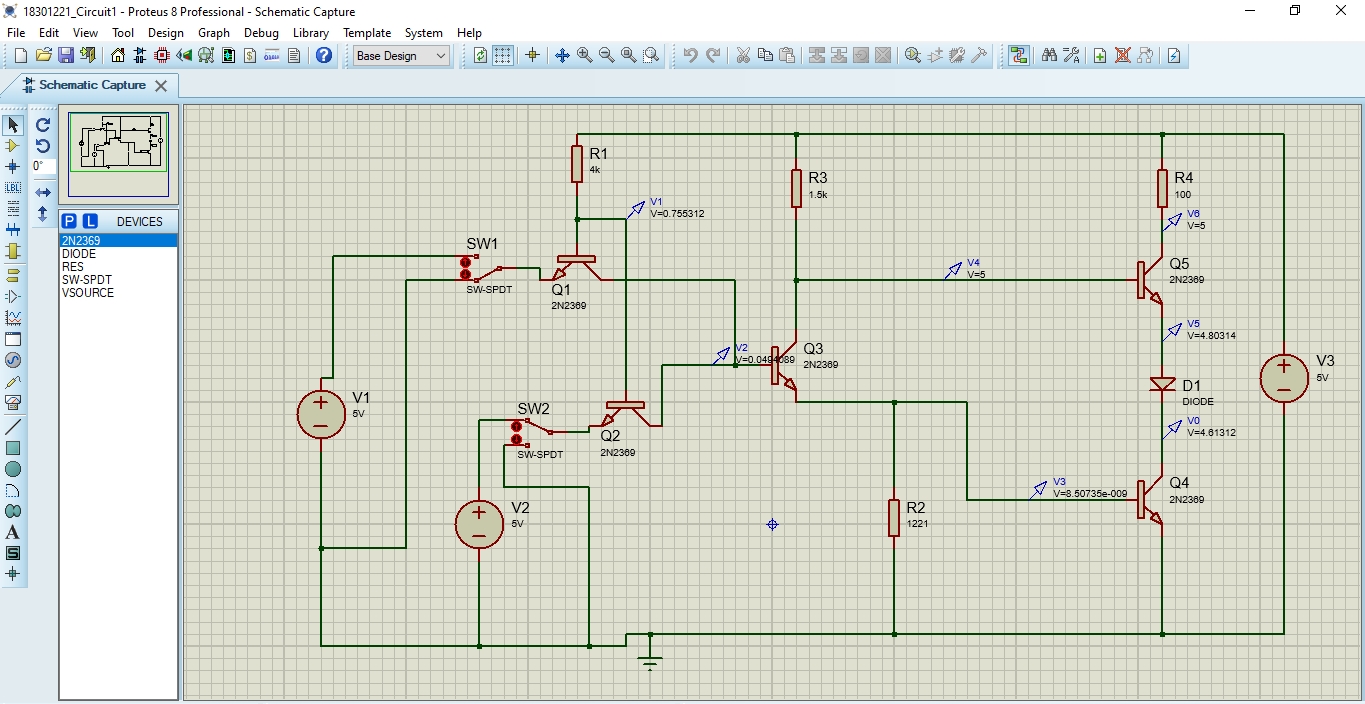
**Name: MD. Abdul Kahhar Siddiki Shan**

**ID: 18301221**

**Section- 02**

**Date of Submission- 18 July,2021**

**Circuit diagram of TTL-NAND gate:**

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